



# EN0-001<sup>Q&As</sup>

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**QUESTION 1**

A program running on a development board that is connected to a host using a debugger can access a file on the host by using:

- A. Memory mapping
- B. Semihosting
- C. Polling
- D. Virtual I/O

Correct Answer: B

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**QUESTION 2**

In an experiment, the time taken for an application to complete a given task is measured using a stopwatch. Which THREE of the following make up the total time? (Choose three)

- A. The time spent waiting for I/O operations
- B. The time taken to download the program via the debugger
- C. The time taken for memory accesses
- D. The time taken for the CPU to execute instructions
- E. The time taken to compile the source code
- F. The time taken to perform instruction tracing

Correct Answer: ACD

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**QUESTION 3**

The ARMv7-A virtual memory management system supports 32-bit (short) and 64-bit (long) page table descriptors. The sizes of a small page in a short descriptor and a small page in a long descriptor are:

- A. 1 KB and 4KB respectively
- B. 4KB and 4KB respectively
- C. 4KB and 16KB respectively
- D. 16KB and 16KB respectively

Correct Answer: B

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**QUESTION 4**

In the Generic Interrupt Controller (GIC), when an interrupt is requested, but is not yet being handled, it is in which of the following states?

- A. Inactive
- B. Active
- C. Pending
- D. Edge-triggered

Correct Answer: C

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**QUESTION 5**

During an investigation into a software performance problem an engineer doubles the clock frequency of a cached ARM processor running the software. Unfortunately the performance of the application does not increase by very much, despite running on the processor for 100% of the time. What is likely to be the main bottleneck in the system?

- A. The processor is context switching between multiple processes
- B. Performance is limited by the speed of external memory
- C. The processor is taking too long to execute branch instructions
- D. The system is raising interrupts too fast for the processor to handle them

Correct Answer: B

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**QUESTION 6**

An application contains three calls to an external function, `foobar()`, which is defined in a shared (or dynamic) library. How many copies of `foobar()` will the linker place in the application image?

(Ignore linker inlining)

- A. None
- B. Always one
- C. Always three
- D. One or more depending on optimization level

Correct Answer: A

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**QUESTION 7**

The disassembly of a program written in C shows calls to the function `__aeabi_fadd`. Which one of these compiler



floating point options could have been used?

- A. Hard floating-point linkage
- B. Soft floating-point linkage without floating-point hardware
- C. Hard floating-point linkage with optimization for space
- D. Soft floating-point linkage with floating-point hardware

Correct Answer: B

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#### QUESTION 8

In a hardware system that runs software providing secure systems, which of the following describes the behavior of external memory and peripherals?

- A. They are not accessible when the processor is in Non-secure state
- B. They cannot know whether the processor is performing a Secure or Non-secure access
- C. They can use the Secure or Non-secure status of the access to decide what response to give
- D. They are required to give an ERROR response when Secure code accesses Non-secure locations in memory

Correct Answer: C

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#### QUESTION 9

Consider a sequence of five independent instructions running on a pipelined processor. There are no interlocks and no data dependencies between instructions, and each instruction takes one cycle to execute. The processor has three pipeline stages and is not superscalar.

How many cycles does it take to fetch, decode and execute all five instructions in sequence, assuming that there are no pipeline stalls?

- A. 5 cycles
- B. 7 cycles
- C. 8 cycles
- D. 15 cycles

Correct Answer: B

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#### QUESTION 10

When linking with the standard C library, which library functions MUST be redefined in order to port your code to a new piece of production hardware?



- A. Functions that are not compliant with the ISO C library standard
- B. Functions that are not compliant with the 1985 IEEE 754 standard for binary floating-point arithmetic
- C. Target-dependent functions which use semihosting
- D. Functions called implicitly by the compiler

Correct Answer: C

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#### QUESTION 11

Which of the following operations would count as intrusive to normal processor operation?

- A. Tracing using Embedded Trace Macrocell (ETM)
- B. Halt mode debugging
- C. Monitor mode debugging
- D. Using the Performance Monitor Unit

Correct Answer: B

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#### QUESTION 12

An ARM Cortex-A9 multi-core system has two CPUs, C1 and C2, each with a corresponding data cache. The code running on C1 writes to a memory location M. and C1 updates its data cache, but not main memory. After that, C2 tries to read the contents of memory location M. Which of the following hardware can automatically (without software intervention) ensure that C2 reads the updated contents of M?

- A. Snoop Control Unit
- B. Tightly Coupled Memory
- C. Level 2 Cache Controller
- D. Dynamic Memory Access Controller

Correct Answer: A

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