HP2-T16$^{Q\&As}$

Industry Standard Architecture and Technology

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QUESTION 1

Which type of information is obtained during the site survey? (Select two)

A. biggest IT problem today

B. capacity of electrical circuits

C. projected role of the server

D. facility size

Correct Answer: BD

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 2 - Page 99:\par Conducting a site survey\par To evaluate these factors, you can use survey questions to gather data, as demonstrated by the following examples:\par How large is the facility?\par Does the facility currently have any radio frequency interference (RFI) problems?\par Is there any extra space?\par Will an existing space need to be modified?\par Are adequate utility outlets available in the proposed space?\par Are the electrical circuits of sufficient capacity?\par }

QUESTION 2

Your server is running Windows 2003 and you are experiencing network performance issues. What can you do to determine if the bottleneck is the network card?

A. Replace the NVRAM on the network card.

B. If the server contains multiple network cards, remove all but one card.

C. Run the system monitor applet to monitor the network throughput.

D. Add NVRAM to the network card.

Correct Answer: C

QUESTION 3

What factors are important to sustain the I/O rates of the application within an array? (Select two)

A. capacity of the disks within the array

B. speed of the disks within the array

C. number of disks within the array

D. location of the disks within an enclosure

Correct Answer: BC

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\viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 249:\par Number of disks in an array\par The RAID level and the number of drives affect the available I/O bandwidth for a given disk configuration. You must have enough drives (regardless of the drive capacity) to sustain the I/O rates of the application. \par }

QUESTION 4

In the GFS backup tape rotation plan, which type of backup is the Grandfather?

A. copy

B. normal

C. differential

D. incremental

Correct Answer: B

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QUESTION 5

In systems with AMD processors, what allows communication between processors and the I/O subsystem?

A. Northbridge

B. Southbridge

C. HyperTransport link

D. APIC

E. QuickPath Interconnect

Correct Answer: C

{\rtf1\ansi\ansicpg936\deff0\deflang1033\deflangfe2052{\fonttbl{\f0\fnil\fcharset0 MS Shell Dlg 2;}} \viewkind4\uc1\pard\lang2052\f0\fs17 Industry Standard Architecture - Student Guide 1 - Page 37\par AMD processors are able to communicate with each other through HyperTransport point-to-point links. This enables one processor to access the memory connected to another processor. Inside the processor, a crossbar switch connects the processor, memory controller, and HyperTransport links.\par AMD processors also use HyperTransport links to connect to the I/O subsystem.\par The links on particular processors are connected to I/O tunnels that support the I/O devices.\par All other processors can communicate with the I/O system through the HyperTransport links.\par Legacy devices are also connected to one of the I/O tunnels.\par }

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