



# EN0-001<sup>Q&As</sup>

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**QUESTION 1**

The size of a C `int` type in the ARM architecture is:

- A. 8 bits
- B. 16 bits
- C. 32 bits
- D. 64 bits

Correct Answer: C

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**QUESTION 2**

Is it possible to use an interrupt controller based on the Generic Interrupt Controller (GIC) architecture in a device built around a single core Cortex-A9 MPCore processor?

- A. No, they are completely incompatible
- B. Yes, all Cortex-A9 MPCore processors include an integrated GIC
- C. Yes, but a dummy second processor has to be included
- D. No, a GIC is only compatible with multi-core Cortex-A9 processors

Correct Answer: B

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**QUESTION 3**

A re-entrant interrupt handler would typically be used to:

- A. Allow an external interrupt to interrupt an SVC handler
- B. Reduce response time for higher priority interrupts
- C. Allow an interrupt handler to be relocated in memory
- D. Avoid the need for an interrupt handler to use a stack.

Correct Answer: B

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**QUESTION 4**

When a linker is removing unused sections during a static link (for example, `-remove` or `-gc` sections), it finds the sections to keep by following all relocations starting from:

- A. The entry point(s).



- B. The function named '\\main\\'.
- C. All local functions and variables.
- D. The reset vector.

Correct Answer: A

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#### QUESTION 5

In a Cortex-A9 processor, CP14 system control registers are used for:

- A. Cache control operations
- B. Address translation operations
- C. Debug control and status information
- D. Architecture feature ID registers

Correct Answer: C

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