EN0-001^{Q&As}

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QUESTION 1

According to the EABI. what would the C size of () operator return when given the following structure?

```
struct foo
{
    char        field_1;
    short       field_2;
    int         field_3;
    int         field_4;
    long long   field_5;
};
```

A. 19

B. 20

C. 24

D. 28

Correct Answer: C

QUESTION 2

Which of the following would enable the use of a symmetric multiprocessing (SMP) operating system?

A. A dual-core Cortex-A9 processor

B. A Cortex-R4 processor with a Cortex-M3 system controller

C. A Cortex-A8 processor with a graphics processing unit (GPU)

D. A uni-core Cortex-A5 processor with a digital signal processor (DSP)

Correct Answer: A

QUESTION 3

Consider the following code sequence, executing on a processor which implements ARM Architecture v7

A.

LDR r0, [r1]

STR r0, [r2]

STR r3, [r3]

R1 points to a location in normal memory. R2 and R3 point to device memory.

Which of the following statements best describes the ordering rules which apply to this sequence?

A. The two writes to device memory will happen in program order, but the read can be performed out of order

B. The memory accesses can happen in any order

C. The memory accesses will happen in program order

D. The read to r0 and the write from r0 will happen in program order, but the write from r3 can be performed out of order

Correct Answer: C

QUESTION 4

The ARMv7-A virtual memory management system supports 32-bit (short) and 64-bit (long) page table descriptors. The sizes of a small page in a short descriptor and a small page in a long descriptor are:

A. 1 KB and 4KB respectively

B. 4KB and 4KB respectively

C. 4KB and 16KB respectively

D. 16KB and 16KB respectively

Correct Answer: B

QUESTION 5

In a loop termination test, how might a programmer indicate to the compiler that the loop iteration count limit is divisible by four?

A. AND the count limit with -0x3

B. Add 4 to the count limit

C. Subtract 4 from the count limit

D. Shift the count limit left two bit positions

Correct Answer: A