



# EN0-001<sup>Q&As</sup>

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**QUESTION 1**

If a Generic Interrupt Controller (GIC) implements 64 priority levels, which priority field bits hold the priority value?

- A. bits [5:0]
- B. bits [7:2]
- C. bits [15:10]
- D. bits [31:26]

Correct Answer: B

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**QUESTION 2**

Which TWO of the following options can the ARM Compiler (armcc) directive `__packed` be used for? (Choose two)

- A. To tell the compiler to use only Thumb code
- B. To tell the compiler to produce code of minimum size
- C. To tell the compiler to use the v6 SIMD pack/unpack instructions
- D. To tell the compiler that an object can be on an unaligned address
- E. To tell the compiler not to perform padding inside structures

Correct Answer: DE

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**QUESTION 3**

Many ARM cores provide two instruction sets, ARM and Thumb. Which THREE of the following statements apply to the Thumb instruction set implemented for the ARMv7-A architecture? (Choose three)

- A. Thumb is a hybrid 16/32-bit instruction set
- B. No Thumb instructions can be conditionally executed
- C. Thumb code is always slower than the equivalent ARM code
- D. Some routines take more instructions in Thumb code than in the equivalent ARM code
- E. The Thumb instruction set can access the Advanced SIMD "NEON" instructions
- F. Thumb code is always more power-efficient than equivalent ARM code

Correct Answer: ADE

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#### QUESTION 4

In which of these cases would code have better performance when compiled for Thumb state than when compiled for ARM state?

- A. When the processor has no data cache
- B. When the code involves many shifting operations
- C. When the code has many conditionally executed instructions
- D. When the processor can only fetch instructions 16-bits at a time

Correct Answer: D

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#### QUESTION 5

In an MPCore system, when one core is waiting for resources to be released, what instruction could be used to reduce that core's power consumption?

- A. WFE
- B. PLD
- C. NOP D. DSB

Correct Answer: A

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