# EN0-001 Q&As

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**QUESTION 1**

In an ARMv7-A processor, with which level of the memory system is the Memory Management Unit (MMU) associated?

A. Level 1

B. Level 2

C. Level 3

D. Level 4

Correct Answer: A

**QUESTION 2**

When debugging an embedded Linux system, which one of the following techniques can be used to halt a single user thread, while allowing other threads to continue to run during the debug process?

A. Halting a single user thread in an embedded Linux system is not possible

B. Use the Linux kernel printk() function to output messages to the console

C. Connect a Linux-aware JTAG debugger to the target, which allows single-stepping of the code

D. Connect a debugger running on an external host device to an instance of gdbserver running on the target, using Ethernet

Correct Answer: D

**QUESTION 3**

If the performance of an application remains unchanged when the core clock speed of a Cortex-A9 processor is reduced, what can you deduce about the system?

A. The Clocks Per Instruction (CPI) of the processor has increased

B. The processor is NOT the limiting factor on performance

C. Instruction cache utilization has improved

D. The core has stopped carrying out speculative data memory accesses

Correct Answer: B

**QUESTION 4**

Which of the following statements is TRUE with respect to the power consumption related to memory accesses?

A. Accessing a large memory device consumes less power than accessing a small one

B. A series of non-sequential accesses is more efficient than a series of sequential accesses

C. Increasing the size of the cache will always reduce power consumption for a given application

D. Storing frequently used data in Tightly Coupled Memory will reduce power consumption

Correct Answer: D

**QUESTION 5**

The Performance Monitoring Unit (PMU) of a Cortex-A9 processor permits direct measurement of which one of the following?

A. Cache Size

B. Clock Speed

C. Program size

D. Numbers of instructions executed

Correct Answer: D