



# EN0-001<sup>Q&As</sup>

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**QUESTION 1**

Which one of the following features must any processor support to conform to the ARMv7-A architecture?

- A. NEON (Advanced SIMD)
- B. Thumb-2 technology
- C. TrustZone (Security Extensions)
- D. Generic Interrupt Controller

Correct Answer: B

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**QUESTION 2**

An ARM processor connected to a Generic Interrupt Controller (GIC) is handling an active interrupt 11. A new interrupt 12 that is received at the GIC is forwarded to the processor, and the active interrupt 11 is preempted. Which of the following possible values of 11's priority (P1), 12's priority (P2) and the processor's priority mask (PM) permit this to happen? Assume there are 256 priority levels implemented.

- A. P1 = 0x0F, P2 = 0x10, PM = 0xFF
- B. P1 = 0x10, P2 = 0x0F, PM = 0xFF
- C. P1 = 0x0F, P2 = 0x10, PM = 0x0
- D. P1 = 0x10, P2 = 0x0F, PM = 0x0

Correct Answer: B

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**QUESTION 3**

An advantage of native compiling over cross compiling is that:

- A. It can enable the final code to be smaller, and execute more quickly.
- B. It allows greater parallelism when building code by utilizing many processors.
- C. The compiler is able to produce error and warning messages in a range of languages.
- D. Build scripts can detect details of the target, and automatically configure the build to match.

Correct Answer: D

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**QUESTION 4**

Why does Device memory prohibit speculative accesses?



- A. Speculative accesses might waste energy
- B. Speculative accesses might reduce performance
- C. Speculative accesses might cause unwanted cache coherency traffic
- D. Speculative accesses might cause undesired system state changes

Correct Answer: D

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#### QUESTION 5

What is an "Entry point" in an application?

- A. A place where execution can start
- B. The location of the main () function
- C. The lowest address contained in a program image
- D. A location where the linker can store additional information

Correct Answer: A

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