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**QUESTION 1**

When timing a critical function for an algorithm, using platform time functions such as `get time of day ()`, the result is unpredictable; there is significant variance in the measured time between different runs of the benchmark. Which of the following strategies would improve the accuracy of the measurement?

- A. Time multiple executions of the algorithm and average the result
- B. Break the algorithm into smaller pieces and time them individually
- C. Run the code on a software model of the platform and collect the results on that system
- D. Add some code with a known overhead to the algorithm to make it run slower, and remove the overhead afterwards

Correct Answer: A

QUESTION 2

Which TWO of the following interrupt types does a Generic Interrupt Controller (GIC) support? (Choose two)

- A. Interrupt from a private peripheral to a processor
- B. Interrupt from a processor to a private peripheral
- C. Interrupt from a shared peripheral to a processor
- D. Interrupt from a processor to a shared peripheral
- E. Interrupt from a private peripheral to a shared peripheral
- F. Interrupt from a shared peripheral to a private peripheral

Correct Answer: AC

QUESTION 3

Which one of the following statements is TRUE for software breakpoints?

- A. Limited software breakpoints can be placed in code running from ROM
- B. Each software breakpoint requires one watchpoint resource in the debug hardware
- C. Each software breakpoint requires one breakpoint resource in the debug hardware
- D. The number of available software breakpoints is not limited by the debug hardware

Correct Answer: D

QUESTION 4



Which of the following instructions can be used to enter a power saving mode?

- A. PLD
- B. PLI
- C. WFE
- D. DSB

Correct Answer: C

QUESTION 5

The Cortex-A9 processor has 6 breakpoint units and 4 watchpoint units. What is the maximum number of breakpoints the debugger can set on code in ROM?

- A. 6
- B. 10
- C. 2
- D. The debugger can use the BKPT instruction to do this.

Correct Answer: A

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