



# EN0-001<sup>Q&As</sup>

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**QUESTION 1**

A standard performance benchmark is being run on a single core ARM v7-A processor. The performance results reported are significantly lower than expected. Which of the following options is a possible explanation?

- A. L1 Caches and branch prediction are disabled
- B. The Embedded Trace Macrocell (ETM) is disabled
- C. The Memory Management Unit (MMU) is enabled
- D. The Snoop Control Unit (SCU) is disabled

Correct Answer: A

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**QUESTION 2**

When using the Performance Monitoring Unit to count runtime events the counter registers are limited to 32-bits. How can more than 2A32 events be counted without significantly impacting the software performance?

- A. Register an interrupt which is triggered when the counter overflows
- B. Count the events using a 64-bit VFP register
- C. Allow one event type to use concatenated counter registers
- D. Poll the event counter, resetting it when the counter is close to overflowing

Correct Answer: A

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**QUESTION 3**

In which TWO of the following locations would a compiler typically place local variables? (Choose two)

- A. ROM
- B. Heap
- C. Cache
- D. Registers
- E. Stack

Correct Answer: DE

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**QUESTION 4**

An embedded application running on an ARM processor is not meeting its expected performance target. The target



hardware on which the application is running allows the frequency of the CPU to be increased independently from the memory system.

The CPU frequency is increased from 800 MHz to 1 GHz and experiments verify that the application performance does not increase.

Which one of the following statements MUST BE TRUE?

- A. The performance is limited by something other than the CPU
- B. The cache hit rate has gone down
- C. The measurement methodology is flawed and the experiment needs to be repeated
- D. The operating system is performing more context switches

Correct Answer: A

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#### QUESTION 5

When building code for both ARM and Thumb states, which tool decides for each function call whether to use a BL or BLX instruction?

- A. The linker
- B. The archiver
- C. The compiler
- D. The assembler

Correct Answer: A

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