

DEA-41T1^{Q&As}

Associate - PowerEdge

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QUESTION 1

Which error control technique is used by ECC memory?

- A. Repetition codes
- B. Hamming code
- C. Checksums
- D. Cryptographic hash functions

Correct Answer: B

Reference: https://en.wikipedia.org/wiki/ECC_memory

QUESTION 2

Which is an example of Host Channel Adapter?

- A. GPGPU PCIe card
- B. PERC in PCIe adapter form factor with external connectors
- C. InfiniBand PCI card
- D. An adapter that combines functionality of Ethernet and FC

Correct Answer: C

Reference: https://www.webopedia.com/definitions/host-channel-adapter/

QUESTION 3

Which component determines the system board chipset?

- A. SFP
- B. HDD
- C. M.2
- D. CPU

Correct Answer: D

QUESTION 4

Where in the iDRAC GUI can the recording of the last three boot cycles of a Dell EMC PowerEdge server be found?



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- A. Maintenance > Troubleshooting
- B. Maintenance > Diagnostics
- C. Troubleshooting > SupportAssist
- D. Troubleshooting > Lifecycle Log

Correct Answer: A

Reference: https://www.dell.com/support/manuals/en-pk/poweredge-r540/idrac_3.30.30.30_ug/viewing- boot-and-crash-capture-videos?guid=guid-56ced8f4-162d-4329-ab95-169445213eaeandlang=en-us

QUESTION 5

A customer looks for a storage solution to boot the hypervisor, where one is active and the other standby. Which solution will allow the requirement?

- A. BIOS
- B. IDSDM
- C. BOSS
- D. UEFI

Correct Answer: C

Reference: https://i.dell.com/sites/doccontent/shared-content/data-sheets/en/Documents/Dell-PowerEdge-Boot-Optimized-Storage-Solution.pdf

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