



C1000-031^{Q&As}

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**QUESTION 1**

Which of the following is a benefit of Power Enterprise Pools?

- A. Has no restrictions on where or how long cores and memory can be relocated.
- B. Provides automatic load balancing based on built in policies enabled in PowerVM.
- C. Can use a Scale-out system temporarily in certain conditions.
- D. Provides flexibility to manage both core and memory resources across multiple servers.

Correct Answer: D

Reference: https://www.ibm.com/support/knowledgecenter/TI0003N/p8ha2/systempool_cod.htm

QUESTION 2

A customer has a 40 core Power System E980 and would like to use Mobile Capacity on Demand.

What is the minimum of static processor activations that are required?

- A. 10
- B. 12
- C. 20
- D. 8

Correct Answer: D

Reference: <https://www-01.ibm.com/common/ssi/cgi-bin/ssialias?htmlfid=899/ENUSLG180083andinfotype=ANandsubtype=CA>

QUESTION 3

A client wants to relocate partitions between servers to avoid application downtime during system maintenance or to automatically balance workloads.

Which offerings provide these capabilities?

- A. PowerHA Enterprise Edition with PowerVM Enterprise Edition
- B. PowerVC Standard Edition with PowerVM Enterprise Edition
- C. PowerHA Enterprise Edition with PowerVC Enterprise Edition
- D. PowerSC Standard Edition with PowerVC Enterprise Edition

Correct Answer: B



QUESTION 4

A customer is considering implementing NovaLink for critical Enterprise workloads.

Where will NovaLink run?

- A. As an appliance connected to the FSP.
- B. On the HMC.
- C. As a job in each dual VIOS.
- D. In a separate Linux partition on the managed server.

Correct Answer: D

QUESTION 5

What is L4 cache?

- A. Off-processor-chip buffering to provide faster main memory access in enterprise servers.
- B. Allows for Active Memory Expansion of the main memory in enterprise servers
- C. On-processor-chip buffering of GPU results
- D. On-processor-chip buffering between L3 and POWER9 processor cores

Correct Answer: D

Reference: <https://superuser.com/questions/1073937/what-does-l4-cache-hold-on-some-cpus>

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